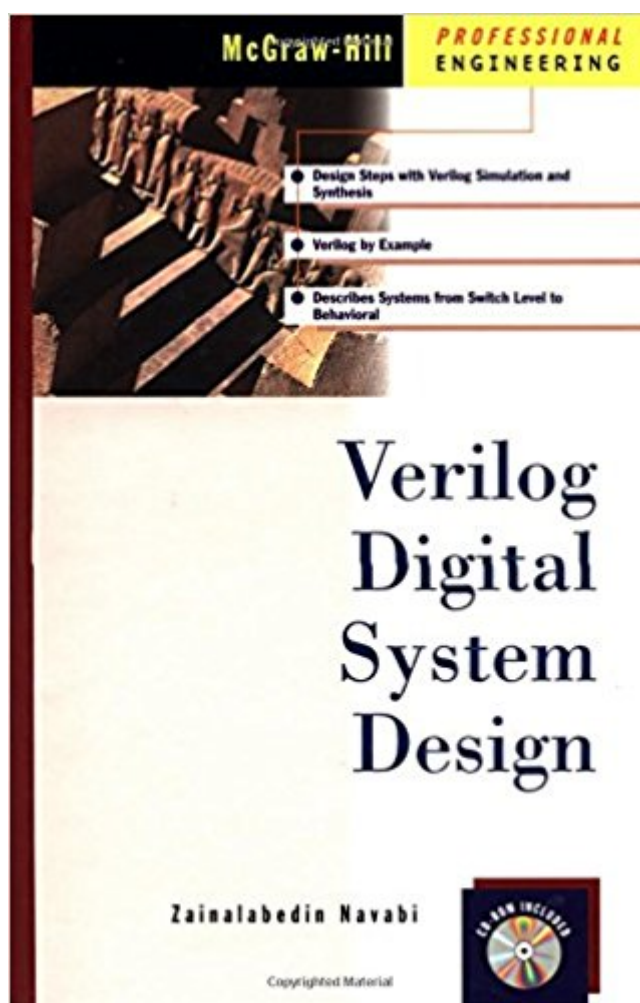The book was found

Verilog Digital System Design With CDROM (McGraw-Hill Professional Engineering)

Synopsis

Hardware description languages are used to design high density VLSI, allowing designers to put millions of transistors on a single substrate. Verilog is closing in on VHDL as the most popular of the hardware description languages. This is a tutorial in designing with Verilog. Each major chapter features in-depth Verilog examples of the coding described, and all the examples are assembled into a final full CPU design. The CD-ROM includes a Verilog simulator and all examples worked in the book.

Book Information

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Customer Reviews

The book comes with a CD that contains Verilog and VHDL simulators, lecture material, and a web based electronic indexing tool that we call Verilog Circuit Navigator. To see how the navigator works, please send email to navabi@ece.neu.edu for a demo version.

LEARN VERILOG DESIGN WITH A MASTER This rigorous tutorial shows electronics designers and students how to apply Verilog in sophisticated digital systems design, using over a hundred skill-building, fully worked-out, and simulated examples. Completely updated, the second edition covers Verilog 2001, new synthesis standards, testing and testbench developments, and the new OVL verification library. You'll find out just what's involved in using Verilog hardware description language (HDL) in digital system design. HDL expert Zain Navabi explains the design process in

logical sequence -- the way it's done in the real world. Moving from simple concepts to the more complex, Navabi interprets Verilog constructs related to design stages and design abstractions, including behavioral description, dataflow description, and structure description. With emphasis on the concepts of concurrency and delay in hardware, the text helps you grasp the essence of HDLs. Clear specification of learning objectives at the beginning of each chapter and end-of-chapter problems focus attention on key concepts. If mastery of design with Verilog is the goal, Zain Navabi's Verilog Digital System Design is the tool. MUST-HAVE CD INCLUDED: Verilog and VHDL simulators Synthesis tools Mixed-level logic and Verilog design environment FPGA design tools and environments from Altera Related tutorials and standards All worked examples from the book, including testbench and simulation-run reports for every example Complete CPU examples with Verilog code and software tools OVL verification libraries and tutorials THE BEST VERILOG TUTORIAL -- UPDATED: Verilog 2001, step by step in examples and text OVL verification library New synthesis standards New chapter on testbench development and verification Problem set in each chapter encourages review and test of key concepts Instructor's manual and lecture slides available for class use --This text refers to an out of print or unavailable edition of this title.

Great book, very detailed, good examples easy to read.

Good book concerning verilog basic concepts. Very useful for the begginner student. CD-ROM is also useful thus contains all the code that is in the book pages.

Cheep and very good introduction into Verilog. I'm currently using this book very heavily. I look at the examples very heavily, but haven't really read it strait through. I jump all over the place depending on what my implementation requires.

If you want to learn Verilog but you're not familiar with logicdesign, then this book is just what you're looking for! Not only the hardware description language but also many useful basic logic constructs are introduced in this book. Most other books on Verilog only concentrate on the language itself, and often only the synthesizable part of the language, it might be usful for people who know logic design very well, but it will be difficult for those who want to learn the language and design at the same time. Many examples are presented in the book, including full-adder, shifter registers.... Also the book presents a simple CPU model as well as it's peripherals, so reader can also learn computer architecture and how to model these structures withVerilog. I think this is an excellent text

book to use in a university.I think it'll be great if there can be a book like this that give you knowledge of both logic design and hardware language but focus more on people who have experience with hardware design. Engineers will love a book that target to their needs, a reference book they can look at for problems in their design works. Engineers want to open a book and find what they're looking for quickly,they don't want to read the book from front to back. So the book should be different than a text book used in a university.I'm sure if such a book can be published soon,it'll be very usful to the IT community.

If you are looking for a book that teaches you hardware design with Verilog, this is the one you must have, for the following reasons:1. The flow of the book is from introductory to complex, very reader-friendly, especially for beginners. The content is easy to absorb, which covers hardware design & synthesis, Verilog language details, and computer organization concepts.2. The book is not just an explanation of Verilog language. It actually illustrates the concepts of hardware system design with Verilog: it interprets Verilog constructs related to various design stages and design abstractions: behavioral description, dataflow description, and structure description. It emphasizes the concepts of concurrency and delay in hardware, which is very important in grasping the gist of HDLs.3. There are abundant examples all well written for various design situations. The examples range from simple to complex, from an adder to a complete CPU. Most of example code can be used in real-world designs, or as templates, especially the state machine examples, and the test bench examples. A CD containing all the examples is supplied with the book.4. If you ever want to be a bi-linguist (Verilog and VHDL) or a translator, the structure of this book parallels with the author's VHDL book. Most of the examples are available in both languages. As a result, you can learn the other language easily. I keep both books handy for my daily work.5. There is a problem set in each chapter. They are very helpful in reviewing and examining the key points of the chapter.

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Verilog is probably the most common package for modelling digital circuits. Navabi provides detailed instructions on how to use it for many types of circuits. We see how Verilog can provide different levels of modelling. From the Register Transfer Level to doing higher level synthesis. This lets you simulate either a small circuit, to great depth, or to scale to much larger sets of transistors.The testbenching ability of Verilog is emphasised. Vital in checking the validity of performance of your circuits, before you tape out to silicon. One of Verilog's strengths is how its testbenching can save you time and money. Provided you take full advantage of it.

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